

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. Gabara 81-10-1-14

In re application of: Thaddeus J. Gabara, Inkyu Lee, Marissa L. Lopez-Vallejo, and Syed Mujtaba

Patent No.: US 7,353,450 B2

Issued: 04/01/2008

Matter No.: 992.1002

For: "Block Processing in a Maximum A Posteriori Processor for Reduced Power Consumption"

**PETITION FOR
CERTIFICATE OF CORRECTION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a Certificate of Correction listing errors that appear in the claims section of above-referenced patent.

1. In column 7, lines 10 to 13 replace,

$A_j^k = \max_{i=0,1,\dots,7} (A_i^{k-3} + \tilde{F}_{i,j}^k)$ for $j = 0, 1, \dots, M-1$," with

-- $A_j^k = \max_{i=0,1,\dots,7} (A_i^{k-N} + \tilde{F}_{i,j}^k)$ for $j = 0, 1, \dots, M-1$,". Support for this correction is found on

page 3, line 13, of the amendment filed on 02/12/2007.

[X] The Commissioner is hereby authorized to charge payment of any fees required under 37 CFR §§ 1.20(a) associated with this communication or credit any overpayment to **Mendelsohn & Associates, P.C. Deposit Account No. 50-0782**.

Respectfully submitted,

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Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : US 7,353,450 B2
DATED : Apr. 1, 2008
INVENTOR(S) : Thaddeus J. Gabara, Inkyu Lee, Marissa L. Lopez-Vallejo, and
Syed Mujtaba

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In column 7, lines 10 to 13 replace, “ $A_j^k = \max_{i=0,1,\dots,7} (A_i^{k-3} + \tilde{\Gamma}_{i,j}^k)$ for $j = 0, 1, \dots, M-1$,” with
-- $A_j^k = \max_{i=0,1,\dots,7} (A_i^{k-N} + \tilde{\Gamma}_{i,j}^k)$ for $j = 0, 1, \dots, M-1$ --.

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PATENT NO. 7,353,450

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